

Official

7/9/03

IN THE SPECIFICATION:

In the title, please replace the title from:

**[METHOD AND APPARATUS FOR CONFIGURING AN INPUT/OUTPUT
BUFFER FOR AN INTEGRATED BUS BRIDGE AND GRAPHICS CONTROLLER]**

To:

METHOD AND APPARATUS FOR CONFIGURING AN INTEGRATED BUS

Please replace the paragraph on page 5, beginning at line 19, with the following rewritten paragraph:

B1
The northbridge 20 issues graphics data over the AGP bus 90. The AGP bus 90 couples both the external circuit 30 and the external circuit 50 (residing within an expansion slot, not shown) to the northbridge 20. [.] The portion of the AGP bus 90 that couples the external circuit 50 to the northbridge 20 is the full length allowed by the specification. The external circuit 30 is close to the same end of the AGP bus 90 as the northbridge 20, so the propagation delay between the external circuit 30 and the northbridge 20 is relatively short. However, the AGP bus 90 also returns a reflection signal from the external circuit 50 to the external circuit 30. The round-trip propagation delay between the northbridge 20 and the external circuit 30 is approximately twice as long as the trip to the external circuit 50 (i.e., the full length allowed by the specification). This propagation delay postpones a time at which the signal at [theinternal] the internal circuit 30 reaches full magnitude.